

U.S. PATENT AND TRADEMARK OFFICE  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re application of:

DALSON YE SENG KIM  
JEFFREY TOH TUCK FOOK  
LEE CHOON KUAN

Serial No.: 10/797,807

Art Unit: 2822

Filing Date: 03/10/2004

Examiner: Rose, Kiesha L.

For: Method For Fabricating Semiconductor  
Component With Chip On Board Leadframe  
(As Amended)

Attorney Docket No. 02-1467.1

---

**AMENDMENT**

**September 16, 2005**

Mail Stop AMENDMENT  
Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This Amendment is in response to the Office Action dated 06/16/2005, having a statutory period for response set to expire on 09/16/2005. Please amend the application as follows.